

Application Serial No. 09/986,731
Amendment dated March 4, 2004
Reply to Office action of December 16, 2003

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 through 15. (Cancelled)

16. (Original) A controller for a planar display panel comprising a common electrode for driving all of display cells together, which constitute a display screen, or for partly driving any plural number of the display cells at a time, and individual electrodes for individually driving the display cells on the cell-by-cell basis, wherein said controller includes a driving circuit for changing luminance in accordance with the number of pulses applied to each of said individual electrodes within a unit time, thereby effecting gradation display.

17. (Amended currently) A The controller for a planar display panel according to Claim 16, wherein said driving circuit effects the gradation display based on control of application of a relatively wide sustaining pulse and a relatively narrow extinguishing pulse which are used as the pulses to be applied to each of said individual electrodes within the unit time.

18. (Amended currently) A The controller for a planar display panel according to Claim 16, wherein said planar display panel is constituted by display modules as constituent elements each comprising a plurality of display units combined into a pattern of row-and-column matrix, said display modules arranged in the horizontal direction are cascaded, and a power supply is connected to said display modules in parallel, and wherein a signal processing circuit for applying control signals to driving circuits of each of said display modules comprises:

an address information storage unit for storing specific address information,

an input signal control unit for allowing input data to pass through said control unit and taking data, which the display module including said control unit is to represent

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by itself, out of a position indicated by the specific address and a display effective signal in the data,

a through data output buffer for outputting the data, which has passed through said input signal control unit, to the adjacent display module cascaded downstream, a memory into which the data taken out of said input signal control unit is written in response to a write control signal, and from which the data is read in response to a read control signal,

a display pulse generator for generating common electrode and individual electrode driving pulses based on the data taken out of said input signal control unit,

a counter for counting the common electrode driving pulse output from said display pulse generator,

a look-up table for converting the number of pulses counted by said counter into a numerical value of gradation data,

a display data generator for outputting individual electrode control data based on comparison between the gradation data from said look-up table and the individual electrode driving display data read from said memory, and

an output buffer for outputting outputs of said display pulse generator and said display data generator to individual electrode driving circuits and common electrode driving circuits.

19 through 28. (Cancelled)